



BANCA D'ITALIA
EUROSISTEMA

**2021 Banca d'Italia Workshop on
“Floating point acceleration for Computational Economics”**

Rome, 8th November

**14:00 CET
(8:00 ET)**

**Welcome address by prof Jesus Fernandez Villaverde
University of Philadelphia**
“What are the next computational challenges for the Applied Economics?”

**14:10 CET
(8:10 ET)**

Prof. Luca Sterpone, Politecnico di Torino
Artificial Intelligence on FPGAs: A New Computing Paradigm

**14:35 CET
(8:35 ET)**

Prof. Marco Domenico Santambrogio, Politecnico di Milano
Democratizing adaptive computing infrastructures: the role of Computer Aided Design frameworks in heterogeneous reconfigurable systems

**15:00 CET
(9:00 ET)**

Prof. Ettore Napoli, Università degli Studi di Salerno
FPGA vs. VLSI, application cases and trade-offs

**15:25 CET
(9:25 ET)**

Dr. Nick Brown, Edinburgh Parallel Computing Center, Research Fellow
Performance and productivity, can latest FPGAs unlock new computational opportunities?

**15:50 CET
(9:50 ET)**

Break

**16:00 CET
(10:00 ET)**

Dr. Manuel Papasidero, Elettronica S.p.a, Roma
Implementation of Scientific Algorithms on FPGA with High Level Synthesis Approach

**16:30 CET
(10:30 ET)**

Dr. Andrea Capitanelli and Dr. Riccardo Russo, Banca d'Italia
Beyond CPUs: experiences in econometric processing at Bank of Italy

**17:00 CET
(11:00 ET)**

Prof. Alessandro Peri, University of Boulder, Colorado
Programming FPGAs for Economics

17:30 CET
(11:30 ET) *Round table with questions and answers*

17:50 CET
(11:50 ET) *Wrap up and end of the workshop*

NB: If presenters agree on that, all sessions will be recorded.

Organizing Committee:

Giuseppe Bruno (Banca d'Italia)
Riccardo Russo (Banca d'Italia)
Juri Marcucci (Banca d'Italia)

Link to the Google registration form: <https://forms.gle/jr6P4tUfvyi4tj6bA>